



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Carsten Noeske **GROUP:** 2124
SERIAL NO: 09/773,154 **EXAMINER:** Chat C. Do
FILED: January 31, 2001
FOR: A COMPUTING UNIT FOR SIGNAL PROCESSING

Assistant Commissioner of Patents
P.O. Box
Alexandria, VA 22313-1450

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SEP 23 2004
Technology Center 2100

Sir:

SECOND AMENDMENT

This amendment is in response to the Official Action dated April 20, 2004.

Please amend the application as follows:

IN THE CLAIMS:

Amend claims 1-11 as follows:

1.(Currently Amended) A ~~computing device on a~~ monolithic integrated circuit for multiplying together a digitized multiplier signal value and a digitized multiplicand signal value, said monolithic integrated circuit ~~computing device~~ comprising:

an input interface that receives said multiplicand and provides a received multiplicand indicative thereof;

a first place shifting device that includes a first logical assignment circuit to shift data bits of said received multiplicand in response to a first shift command signal, and provides a first shifted